

an insulating layer electrically insulating said data line from said gate line;

a¹ cont.
a thin film transistor formed at an intersection of said gate line and said data line, and connected to said gate line and said data line;

a passivation layer formed over the thin film transistor;

a pixel electrode formed on the surface of the passivation layer, but not over said gate electrode to act as a light shielding layer therefor, said pixel electrode providing a gap space over said data line so as not to shield light therefrom; and

a low reflective layer formed on at least a portion of said data line, said data line having no light shielding layer formed thereover.

a²
3. (Amended) The LCD of claim 2, wherein said low reflective layer is formed on said gate electrode.

a³
11. (Amended) The LCD of claim 1, wherein said passivation layer is formed over said gate line, said data line and said low reflective layer and said pixel electrode formed on said passivation layer is connected via a contact hole in said passivation layer to said thin film transistor.

a4
15. (Amended) A method of manufacturing a liquid crystal display,
comprising:

forming a gate line and a portion protruding from said gate line to serve
as a gate electrode of a thin film transistor on a transparent substrate;

forming an insulating layer electrically insulating said gate line;

forming a data line over said transparent substrate and crossing said
gate line;

forming a passivation layer over the thin film transistor;

forming a pixel electrode on the surface of the passivation layer, but not
over said gate electrode to act as a light shielding layer therefor, said pixel
electrode providing a gap space over said data line so as not to shield light
therefrom; and

first forming a low reflective layer over at least a portion of said data line,
said data line having no light shielding layer formed thereover.

a5
17. (Amended) The method of claim 16, wherein said second forming
step forms said low reflective layer over said gate electrode.

a4
22. (Amended) A method of manufacturing a liquid crystal display,
comprising:

forming a gate line and gate electrode connected thereto on a transparent substrate;

forming an insulating layer over said gate line and gate electrode;

forming a semiconductor layer over said gate electrode;

forming a data line crossing said gate line, a source electrode connected to said data line and on a first portion of said semiconductor layer, and a drain electrode on second portion of said semiconductor layer;

forming a low reflective layer over at least a portion of said data line, said data line having no light shielding layer formed thereover;

forming a passivation layer having a contact hole exposing said drain electrode over said transparent substrate; and

forming a pixel electrode on said passivation layer but not over said gate electrode to act as a light shielding layer therefor, said pixel electrode providing a gap space over said data line so as not to shield light therefrom and connected to said drain electrode via said contact hole.